

# **REDUCED COMPLEXITY COMPUTER SYSTEM ARCHITECTURE**

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## **ABSTRACT OF THE DISCLOSURE**

An architecture for a computer system that has lower complexity than prior computer system architectures. In one embodiment the functionality that in prior computer system architectures resides in a central processing unit (CPU) is split between a CPU and a program control unit. This allows the CPU to have lower complexity and can permit the computer system to have greater flexibility and lower cost. In another embodiment the computer system contains a memory mapping unit. The memory mapping unit enables the computer system to use one of a plurality of peripherals without changing the instruction codes of the CPU thus increasing the flexibility of the computer system.

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